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DiSanto et al.

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[54] DUAL ANODE FLAT PANEL ELECTROPHORETIC DISPLAY APPARATUS

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[*] Notice: The portion of the term of this patent subsequent to Oct. 1, 2008 has been dis-

claimed.

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Related U.S. Application Data

[63] Continuation of Ser. No. 719,021, Jun. 21, 1991, Pat. No. 5,302,235, which is a continuation-in-part of Ser. No. 345, 825, May 1, 1989, Pat. No. 5,053,763.

[51] Int. Cl.⁶ G09G 3/34

[52] U.S. Cl. 216/5; 216/67

[56] References Cited

U.S. PATENT DOCUMENTS

5,302,235 4/1994 DiSanto et al. 156/643

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57] ABSTRACT

An electrophoretic display has a grid cathode matrix arrangement consisting of a first plurality of parallel conductive lines insulated from a second plurality of parallel conductive lines transverse to said first plurality. Located with respect to the grid and cathode lines are first and second anode structures. The first anode is remote from the second with the second anode overlying the gird lines of the display and insulated therefrom. The second anode is biased to implement typical HOLD and ERASE modes independent of the first anode.

7 Claims, 1 Drawing Sheet

